

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

Claims 1-32 (canceled)

1 Claim 33 (new): A method for cryptographically processing
2 data, comprising the steps of:
3 a) feeding, to a cryptographic process (P), values of
4 data (X) and a key (K);
5 b) performing the cryptographic process (P) to yield
6 cryptographically processed output data (Y);
7 c) feeding, to the process (P), auxiliary values that
8 mask the data (X) used in the process (P); and
9 d) compensating, by an auxiliary process, influence of
10 the auxiliary values on the output data (Y).

1 Claim 34 (new): A method for cryptographically processing
2 data, comprising the steps of:
3 a) feeding, to a cryptographic process (P), values of data
4 (X) and a key (K);
5 b) performing the cryptographic process (P) to yield
6 cryptographically processed output data (Y);
7 c) feeding, to an invertible supplementary process (P*), a
8 supplementary key (K*) in order to form the key (K); and
9 d) wherein:
10 the supplementary key (K*) masks the key (K) used
11 in the process (P); and

12 the supplementary process (P*) comprises a
13 cryptographic process to which an auxiliary key (K') is fed.

1 Claim 35 (new): The method recited in claim 34 wherein the
2 supplementary key (K*) is obtained from a process that is
3 inverse to the supplementary process (P*) and based on the
4 key (K) and the auxiliary key (K').

1 Claim 36 (new): A method for cryptographically processing
2 data, comprising the steps of:

- 3 a) feeding, to a cryptographic process (P), values of
4 data (X) and a key (K);
5 b) performing the cryptographic process (P) in order to
6 form cryptographically processed output data (Y);
7 c) feeding, to an supplementary process (P*), a
8 supplementary key (K*) in order to form the key (K);
9 d) wherein:

10 the supplementary key (K*) masks the key (K) used
11 in the process (P);

12 the supplementary process (P*) comprises a
13 cryptographic process to which an auxiliary key (K') is fed;
14 the data (X) is also fed to the supplementary
15 process (P*); and

16 the supplementary process (P*) is performed only
17 if the data (X) has predetermined properties.

1 Claim 37 (new): The method recited in claim 33 wherein the
2 cryptographic process (P) comprises a number of steps (S_i),
3 each of said steps (S_i) having a cryptographic operation for
4 processing right-hand data (RD_i) derived from the data (X),
5 so as to yield processed right-hand data (FD_i), and a
6 combinatory operation (CC_i) for combining with left-hand

7 data (LD_i), also derived from the data (X), and the
8 processed right-hand data (FD_i) in order to form modified
9 left data (SD_i'), and wherein right-hand data (RD_i) is
10 combined with a primary auxiliary value (A_i) prior to a
11 first one of the steps (S_i) and left-hand data (LD_i) is
12 combined with an additional auxiliary value (A_0).

1 Claim 38 (new): The method recited in claim 37 wherein
2 immediately after a last one of the steps (S_n), right-hand
3 data (RD_n) is combined with a further primary auxiliary
4 value (A_n) and modified left-hand data (SD_n') is combined
5 with a further additional auxiliary value (A_{n+1}).

1 Claim 39 (new): The method recited in claim 37 wherein the
2 right-hand data (RD_i) is combined, in each one of the
3 steps (S_i) and prior to a cryptographic operation (F_i), with
4 a primary auxiliary value (A_i) of said one step (S_i).

1 Claim 40 (new): The method recited in claim 37 wherein the
2 processed right-hand data (FD_i) is combined, following a
3 cryptographic operation (F_i), with a secondary auxiliary
4 value (B_i) of said one step (S_i).

1 Claim 41 (new): The method recited in claim 40 wherein the
2 secondary auxiliary value (B_i) of one of the steps (S_i) is
3 formed from a combination of a primary auxiliary value (A_{i-1})
4 of a preceding one of the steps and a primary auxiliary
5 value (A_{i+1}) of a next one of the steps.

1 Claim 42 (new): The method recited in claim 37 wherein all
2 primary auxiliary values (A_i) are equal.

1 Claim 43 (new): The method recited in claim 38 wherein the
2 primary auxiliary values (A_i) or secondary auxiliary
3 values (B_i) have been previously combined each time with a
4 respective cryptographic operation (F_i').

1 Claim 44 (new): The method recited in claim 43 wherein a
2 combined cryptographic operation (F_i') contains a plurality
3 of tables; and the tables are determined in a different
4 order each time the cryptographic process (P) is performed.

1 Claim 45 (new): The method recited in claim 43 wherein a
2 combined cryptographic operation (F_i') contains a plurality
3 of tables; and the elements of the tables are determined or
4 stored in a different order each time the cryptographic
5 process (P) is performed.

1 Claim 46 (new): The method recited in claim 45 wherein the
2 order is stored as a lookup table.

1 Claim 47 (new): The method recited in claim 37 wherein the
2 right-hand data (RD_i) is combined with a tertiary auxiliary
3 value (W_i) after each one of the steps (S_i).

1 Claim 48 (new): The method recited in claim 47 wherein the
2 tertiary auxiliary value (W_i) in all steps, except the last
3 one of said steps (S_n), equals a combination of the primary
4 auxiliary value (A_1) of the first one of the steps (S_1) and
5 the additional auxiliary value (A_0); and in the last one of the
6 steps (S_n) the tertiary auxiliary value equals zero.

1 Claim 49 (new): The method recited in claim 37 wherein
2 combining is performed through an exclusive-OR (XOR)
3 operation.

1 Claim 50 (new): The method recited in claim 33 wherein the
2 data (X) comprises identification data of a payment device;
3 and the processed data (Y) forms a diversified key.

1 Claim 51 (new): The method recited in claim 33 wherein the
2 cryptographic process (P) comprises a DES process.

1 Claim 52 (new): The method recited in claim 51, wherein the
2 DES process comprises triple DES.

1 Claim 53 (new): A circuit for performing the method recited
2 in claim 33.

1 Claim 54 (new): A payment card having the circuit recited in
2 claim 53.

1 Claim 55 (new): A payment terminal having the circuit
2 recited in claim 53.

1 Claim 56 (new): The method recited in claim 34 wherein the
2 data (X) comprises identification data of a payment device;
3 and the processed data (Y) forms a diversified key.

1 Claim 57 (new): The method recited in claim 34 wherein the
2 cryptographic process (P) comprises a DES process.

1 Claim 58 (new): The method recited in claim 57 wherein the
2 DES process comprises triple DES.

1 Claim 59 (new): A circuit for performing the method recited
2 in claim 34.

1 Claim 60 (new): A payment card having the circuit recited in
2 claim 59.

1 Claim 61 (new): A payment terminal having the circuit
2 recited in claim 59.

1 Claim 62 (new): The method recited in claim 36 wherein the
2 data (X) comprises identification data of a payment device;
3 and the processed data (Y) forms a diversified key.

1 Claim 63 (new): The method recited in claim 36 wherein the
2 cryptographic process (P) comprises a DES process.

1 Claim 64 (new): The method recited in claim 63 wherein the
2 DES process comprises triple DES.

1 Claim 65 (new): A circuit for performing the method recited
2 in claim 36.

1 Claim 66 (new): A payment card having the circuit recited in
2 claim 65.

1 Claim 67 (new): A payment terminal having the circuit
2 recited in claim 65.